

# Kindle File Format Modern Semiconductor Devices For Integrated Circuits

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Modern Semiconductor Devices for Integrated Circuits-Chenming Hu 2010 Modern Semiconductor Devices for Integrated Circuits, First Edition introduces readers to the world of modern semiconductor devices with an emphasis on integrated circuit applications. KEY TOPICS: Electrons and Holes in Semiconductors; Motion and Recombination of Electrons and Holes; Device Fabrication Technology; PN and Metal-Semiconductor Junctions; MOS Capacitor; MOS Transistor; MOSFETs in ICs—Scaling, Leakage, and Other Topics; Bipolar Transistor. MARKET: Written by an experienced teacher, researcher, and expert in industry practices, this succinct and forward-looking text is appropriate for anyone interested in semiconductor devices for integrated circuits, and serves as a suitable reference text for practicing engineers. Modern Semiconductor Devices for Integrated Circuits-Chenming Hu 2010 For courses in semiconductor devices. Prepare your students for the semiconductor device technologies of today and

tomorrow. Modern Semiconductor Devices for Integrated Circuits, First Edition introduces students to the world of modern semiconductor devices with an emphasis on integrated circuit applications. Written by an experienced teacher, researcher, and expert in industry practices, this succinct and forward-looking text is appropriate for both undergraduate and graduate students, and serves as a suitable reference text for practicing engineers. Modern Semiconductor Devices for Integrated Circuits-Chenming Hu 2010

Devices for Integrated Circuits-H. Craig Casey 1999 This book develops the device physics of the Si and III-V compound semiconductor devices used in integrated circuits. Important equations are derived from basic physical concepts. The physics of these devices are related to the parameters used in SPICE. Terminology is intended to prepare students for reading technical journals on semiconductor devices. This text is suitable for first-year graduate students and seniors in Electrical Engineering; graduate students in Material Science and Chemical Engineering, interested in semiconductor materials; Computer Science students interested in custom VLSI design; and professionals in the semiconductor industry.

Principles of Semiconductor Devices-Sima Dimitrijevic 2012 "This dynamic text applies physics concepts and equations to practical, real-world applications of semiconductor device theory"-- Semiconductor Device Physics and Design-Umesh Mishra 2007-11-28 Semiconductor Device Physics and Design teaches readers how to approach device design from the point of view of someone who wants to improve devices and can see the opportunity and challenges. It begins with coverage of basic physics concepts, including the physics behind polar heterostructures and strained heterostructures. The book then details the important devices ranging from p-n diodes to bipolar and field effect devices. By relating device design to device performance and then relating device needs to system use the student can see how device design works in the real world.

Modern Semiconductor Device Physics-Sze 1998 An in-depth, up-to-date presentation of the physics and operational principles of all modern semiconductor devices The companion volume to Dr. Sze's

classic Physics of Semiconductor Devices, Modern Semiconductor Device Physics covers all the significant advances in the field over the past decade. To provide the most authoritative, state-of-the-art information on this rapidly developing technology, Dr. Sze has gathered the contributions of world-renowned experts in each area. Principal topics include bipolar transistors, compound-semiconductor field-effect-transistors, MOSFET and related devices, power devices, quantum-effect and hot-electron devices, active microwave diodes, high-speed photonic devices, and solar cells. Supported by hundreds of illustrations and references and a problem set at the end of each chapter, Modern Semiconductor Device Physics is the essential text/reference for electrical engineers, physicists, material scientists, and graduate students actively working in microelectronics and related fields.

Semiconductor Devices: Physics and Technology, 3rd Edition-Simon M. Sze 2012-04-23 The awaited revision of Semiconductor Devices:

Physics and Technology offers more than 50% new or revised material that reflects a multitude of important discoveries and advances in device physics and integrated circuit processing. Offering a basic introduction to physical principles of modern semiconductor devices and their advanced fabrication technology, the third edition presents students with theoretical and practical aspects of every step in device characterizations and fabrication, with an emphasis on integrated circuits. Divided into three parts, this text covers the basic properties of semiconductor materials, emphasizing silicon and gallium arsenide; the physics and characteristics of semiconductor devices bipolar, unipolar special microwave and photonic devices; and the latest processing technologies, from crystal growth to lithographic pattern transfer.

Semiconductor Devices & Circuits-U.A.Bakshi A.P.Godse 2008

Semiconductor Physics and Materials Intrinsic and extrinsic semiconductors, Conduction mechanism in extrinsic semiconductors, Carrier concentrations, Drift and diffusion mechanisms, Drift and diffusion current densities, Excess carriers, Recombination process, Mean carrier lifetime, Conductivity, Mobility, Mass action law, Einstein relationship. Semiconductor materials used in optoelectronic devices and modern semiconductor devices and integrated circuits - GaAs, SiGe, GaAsP. Semiconductor

Diodes A brief overview of following types of diodes, their peculiarities and applications Rectifier, Signal, Switching, Power, Tunnel, Shockley, Gunn, PIN. Semiconductor P-N Junction Diode : Open circuited step graded junction, Metallurgical junctions and ohmic contacts, Depletion region, Barrier potential, Forward and reverse biased diode operation. V-I characteristic equation of diode (no derivation). Volt equivalent of temperature, Temperature dependence of V-I characteristics, DC load line. Forward and reverse dynamic resistance, Small signal and large signal diode models. Diode data sheet specifications - PIV, IFMSurge, Iav. Switching Diodes - Diode switching times, Junction capacitances. (No derivations). Field Effect Transistors An overview of different types of FETs viz. JFET, MOSFET, MESFET, Peculiarities of these types and their application areas. JFET : JFET construction, Symbol, Basic operation, V-I characteristics, Transfer characteristics ( Shockley's equation), Cut-off & Pinch-off voltages, Transconductance, Input resistance & Capacitance. Drain to source resistance. Universal JFET bias curve. Biasing arrangements for JFET - Biasing against device variation, Biasing for zero current drift. JFET as voltage controlled current source. JFET data sheet specifications - IDSS, VP, gm, rd, RDS or RD (ON). JFET Amplifiers : CS, CD, CG amplifiers. Their analysis using small signal JFET model. MOSFETs An overview of following MOSFET types - D-MOSFET, E-MOSFET, Power MOSFET, n-MOS, p-MOS and CMOS devices. Handling precautions for CMOS devices. D and E-MOSFET characteristics and parameters, Non ideal voltage current characteristics viz. Finite output resistance, body effect, sub threshold conduction, Breakdown effects and temperature effects. MOSFET biasing, Introduction to MOSFET as VLSI device. Bipolar Junction transistor An overview of different types of BJTs - Small signal and large signal low frequency types, Switching/RF, Heterojunction types. Peculiarities of these types and their application areas. BJT Biasing and Basic Amplifier Configurations : Need for biasing BJT, DC analysis of BJT circuits, Typical junction voltages for cut-off, Active and saturation regions, Voltage divider bias and its analysis for stability factors, Small signal-low frequency h-parameter model, Variation of h-parameters with operating point, Other small signal models, Derivations for CE configuration for  $A_i$ ,

Ri, Ro, Avs, Avs interms of h-parameters, Comparison of performance parameters with CB and CC configurations in tabular form. Need for multistage amplifiers and suitability of CE, CC and CB configurations in multistage amplifiers, Small signal and DC data sheet specifications for BJT. Concept of frequency response, Human ear response to audio frequencies, Significance of Octaves and Dacades. The decibel unit. Square wave testing of amplifiers. Miller's theorem. Effect of coupling, bypass, junction and stray capacitances on frequency response for BJT and FET amplifiers. Concept of dominant pole. N stage cascade amplifier, Band pass of cascaded stages (effect on frequency response). Concept of GBW. (No derivations).

Integrated Microelectronic Devices-J. A. Del Alamo 2017-01-20 A modern take on microelectronic device engineering  
Microelectronics is a 50-year-old engineering discipline still undergoing rapid evolution and societal adoption. Integrated Microelectronic Devices: Physics and Modeling fills the need for a rigorous description of semiconductor device physics that is relevant to modern nanoelectronics. The central goal is to present the fundamentals of semiconductor device operation with relevance to modern integrated microelectronics. Emphasis is devoted to frequency response, layout, geometrical effects, parasitic issues and modeling in integrated microelectronics devices (transistors and diodes). In addition to this focus, the concepts learned here are highly applicable in other device contexts. This text is suitable for a one-semester junior or senior-level course by selecting the front sections of selected chapters (e.g. 1-9). It can also be used in a two-semester senior-level or a graduate-level course by taking advantage of the more advanced sections.

Solar Cells-Chenming Hu 1983

History of Semiconductor Engineering-Bo Lojek 2007-07-28 This book provides a unique account of the history of integrated circuit, the microelectronics industry and the people involved in the development of transistor and integrated circuit. In this richly illustrated account the author argues that the group of inventors was much larger than originally thought. This is a personal recollection providing the first comprehensive behind-the-scenes account of the history of the integrated circuit.

Power Integrity Analysis and Management for Integrated Circuits—Raj Nair 2010-05-07 New Techniques and Tools for Ensuring On-Chip Power Integrity—Down to Nanoscale As chips continue to scale, power integrity issues are introducing unexpected project complexity and cost. In this book, two leading industry innovators thoroughly discuss the power integrity challenges that engineers face in designing at nanoscale levels, introduce new analysis and management techniques for addressing these issues, and provide breakthrough tools for hands-on problem solving. Raj Nair and Dr. Donald Bennett first provide a complete foundational understanding of power integrity, including ULSI issues, practical aspects of power delivery, and the benefits of a total power integrity approach to optimizing chip physical designs. They introduce advanced power distribution network modeling, design, and analysis techniques that highlight abstraction and physics-based analysis, while also incorporating traditional circuit- and field-solver based approaches. They also present advanced techniques for floorplanning and power integrity management, and help designers anticipate emerging challenges associated with increased integration. Anasim RLCsim.exe, a new tool for power integrity aware floorplanning, is downloadable for free at [anasim.com/category/software](http://anasim.com/category/software). The authors Systematically explore power integrity implications, analysis, and management for integrated circuits Present practical examples and industry best practices for a broad spectrum of chip design applications Discuss distributed and high-bandwidth voltage regulation, differential power path design, and the significance of on-chip inductance to power integrity Review both traditional and advanced modeling techniques for integrated circuit power integrity analysis, and introduce continuum modeling Explore chip, package, and board interactions for power integrity and EMI, and bring together industry best practices and examples Introduce advanced concepts for power integrity management, including non-linear capacitance devices, impedance modulation, and active noise regulation Power Integrity Analysis and Management for Integrated Circuits' coverage of both fundamentals and advanced techniques will make this book indispensable to all engineers responsible for signal integrity, power integrity, hardware, or system design—especially those working at the nanoscale level.

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Fundamentals of Electronics: Book 1-Thomas F. Schubert  
2015-05-01 This book, *Electronic Devices and Circuit Application*, is the first of four books of a larger work, *Fundamentals of Electronics*. It is comprised of four chapters describing the basic operation of each of the four fundamental building blocks of modern electronics: operational amplifiers, semiconductor diodes, bipolar junction transistors, and field effect transistors. Attention is focused on the reader obtaining a clear understanding of each of the devices when it is operated in equilibrium. Ideas fundamental to the study of electronic circuits are also developed in the book at a basic level to lessen the possibility of misunderstandings at a higher level. The difference between linear and non-linear operation is explored through the use of a variety of circuit examples including amplifiers constructed with operational amplifiers as the fundamental component and elementary digital logic gates constructed with various transistor types. *Fundamentals of Electronics* has been designed primarily for use in an upper division course in electronics for electrical engineering students. Typically such a course spans a full academic year consisting of two semesters or three quarters. As such, *Electronic Devices and Circuit Applications*, and the following two books, *Amplifiers: Analysis and Design* and *Active Filters and Amplifier Frequency Response*, form an appropriate body of material for such a course. Secondary applications include the use in a one-semester electronics course for engineers or as a reference for practicing engineers.

*Fundamentals of Modern VLSI Devices*-Yuan Taur 2013-05-02 Learn the basic properties and designs of modern VLSI devices, as well as the factors affecting performance, with this thoroughly updated second edition. The first edition has been widely adopted as a standard textbook in microelectronics in many major US universities and worldwide. The internationally renowned authors highlight the intricate interdependencies and subtle trade-offs between various practically important device parameters, and provide an in-depth discussion of device scaling and scaling limits of CMOS and bipolar devices. Equations and parameters provided are checked continuously against the reality of silicon data, making the book equally useful in practical transistor design and in the classroom. Every chapter has been updated to include the latest

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developments, such as MOSFET scale length theory, high-field transport model and SiGe-base bipolar devices.

Integrated Silicon Optoelectronics-Horst Zimmermann 2000-04-14

The book covers the entire topic from the basics of optoelectronics, device physics of photodetectors and light emitters, simulation of photodetectors, and technological aspects of optoelectronic integration in microelectronics to circuit aspects and practical applications. It summarizes the state of the art in integrated silicon optoelectronics and reviews recent publications on this topic. Results of basic research on silicon light emitters are included as well, while published results are compared with each other and with the work of the author.

Fundamentals of Electronics: Book 2-Thomas F. Schubert, Jr.

2015-10-05 This book, Amplifiers: Analysis and Design, is the second of four books of a larger work, Fundamentals of Electronics. It is comprised of four chapters that describe the fundamentals of amplifier performance. Beginning with a review of two-port analysis, the first chapter introduces the modeling of the response of transistors to AC signals. Basic one-transistor amplifiers are extensively discussed. The next chapter expands the discussion to multiple transistor amplifiers. The coverage of simple amplifiers is concluded with a chapter that examines power amplifiers. This discussion defines the limits of small-signal analysis and explores the realm where these simplifying assumptions are no longer valid and distortion becomes present. The final chapter concludes the book with the first of two chapters in Fundamental of Electronics on the significant topic of feedback amplifiers. Fundamentals of Electronics has been designed primarily for use in an upper division course in electronics for electrical engineering students. Typically such a course spans a full academic years consisting of two semesters or three quarters. As such, Amplifiers: Analysis and Design, and two other books, Electronic Devices and Circuit Applications, and Active Filters and Amplifier Frequency Response, form an appropriate body of material for such a course. Secondary applications include the use with Electronic Devices and Circuit Applications in a one-semester electronics course for engineers or as a reference for practicing engineers.

III-V Integrated Circuit Fabrication Technology-Shiban Tiku

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2016-04-27 GaAs processing has reached a mature stage. New semiconductor compounds are emerging that will dominate future materials and device research, although the processing techniques used for GaAs will still remain relevant. This book covers all aspects of the current state of the art of III-V processing, with emphasis on HBTs. It is aimed at practicing engineers and graduate students and engineers new to the field of III-V semiconductor IC processing. The book's primary purpose is to discuss all aspects of processing of active and passive devices, from crystal growth to backside processing, including lithography, etching, and film deposition.

Semiconductor Devices : Basic Principles-Jasprit Singh 2007  
Market\_Desc: · Electrical Engineers Special Features: · Over 150 solved examples that clarify concepts are integrated throughout the text. · End-of-chapter summary tables and hundreds of figures are included to reinforce the intricacies of modern semiconductor devices· Coverage of device optimization issues shows the reader how in each device one has to trade one performance against another About The Book: This introductory text presents a well-balanced coverage of semiconductor physics and device operation and shows how devices are optimized for applications. The text begins with an exploration of the basic physical processes upon which all semiconductor devices are based. Next, the author focuses on the operation of the important semiconductor devices along with issues relating to the optimization of device performance.

Radiation Effects and Soft Errors in Integrated Circuits and Electronic Devices-Ronald Donald Schimpf 2004 This book provides a detailed treatment of radiation effects in electronic devices, including effects at the material, device, and circuit levels. The emphasis is on transient effects caused by single ionizing particles (single-event effects and soft errors) and effects produced by the cumulative energy deposited by the radiation (total ionizing dose effects). Bipolar (Si and SiGe), metal-oxide-semiconductor (MOS), and compound semi-conductor technologies are discussed. In addition to considering the specific issues associated with high-performance devices and technologies, the book includes the background material necessary for understanding radiation effects at a more general level.

Integrated Circuits and Semiconductor Devices: Theory and

Application-Gordon J. Deboo 1977

GaAs High-Speed Devices-C. Y. Chang 1994-09-30 The performance of high-speed semiconductor devices—the genius driving digital computers, advanced electronic systems for digital signal processing, telecommunication systems, and optoelectronics—is inextricably linked to the unique physical and electrical properties of gallium arsenide. Once viewed as a novel alternative to silicon, gallium arsenide has swiftly moved into the forefront of the leading high-tech industries as an irreplaceable material in component fabrication. GaAs High-Speed Devices provides a comprehensive, state-of-the-science look at the phenomenally expansive range of engineering devices gallium arsenide has made possible—as well as the fabrication methods, operating principles, device models, novel device designs, and the material properties and physics of GaAs that are so keenly integral to their success. In a clear five-part format, the book systematically examines each of these aspects of GaAs device technology, forming the first authoritative study to consider so many important aspects at once and in such detail. Beginning with chapter 2 of part one, the book discusses such basic subjects as gallium arsenide materials and crystal properties, electron energy band structures, hole and electron transport, crystal growth of GaAs from the melt and defect density analysis. Part two describes the fabrication process of gallium arsenide devices and integrated circuits, shedding light, in chapter 3, on epitaxial growth processes, molecular beam epitaxy, and metal organic chemical vapor deposition techniques. Chapter 4 provides an introduction to wafer cleaning techniques and environment control, wet etching methods and chemicals, and dry etching systems, including reactive ion etching, focused ion beam, and laser assisted methods. Chapter 5 provides a clear overview of photolithography and nonoptical lithography techniques that include electron beam, x-ray, and ion beam lithography systems. The advances in fabrication techniques described in previous chapters necessitate an examination of low-dimension device physics, which is carried on in detail in chapter 6 of part three. Part four includes a discussion of innovative device design and operating principles which deepens and elaborates the ideas introduced in chapter 1. Key areas such as metal-semiconductor contact systems, Schottky Barrier and ohmic contact

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formation and reliability studies are examined in chapter 7. A detailed discussion of metal semiconductor field-effect transistors, the fabrication technology, and models and parameter extraction for device analyses occurs in chapter 8. The fifth part of the book progresses to an up-to-date discussion of heterostructure field-effect (HEMT in chapter 9), potential-effect (HBT in chapter 10), and quantum-effect devices (chapters 11 and 12), all of which are certain to have a major impact on high-speed integrated circuits and optoelectronic integrated circuit (OEIC) applications. Every facet of GaAs device technology is placed firmly in a historical context, allowing readers to see instantly the significant developmental changes that have shaped it. Featuring a look at devices still under development and device structures not yet found in the literature, GaAs High-Speed Devices also provides a valuable glimpse into the newest innovations at the center of the latest GaAs technology. An essential text for electrical engineers, materials scientists, physicists, and students, GaAs High-Speed Devices offers the first comprehensive and up-to-date look at these formidable 21st century tools. The unique physical and electrical properties of gallium arsenide has revolutionized the hardware essential to digital computers, advanced electronic systems for digital signal processing, telecommunication systems, and optoelectronics. GaAs High-Speed Devices provides the first fully comprehensive look at the enormous range of engineering devices gallium arsenide has made possible as well as the backbone of the technology—ication methods, operating principles, and the materials properties and physics of GaAs—device models and novel device designs. Featuring a clear, six-part format, the book covers: GaAs materials and crystal properties Fabrication processes of GaAs devices and integrated circuits Electron beam, x-ray, and ion beam lithography systems Metal-semiconductor contact systems Heterostructure field-effect, potential-effect, and quantum-effect devices GaAs Microwave Monolithic Integrated Circuits and Digital Integrated Circuits In addition, this comprehensive volume places every facet of the technology in an historical context and gives readers an unusual glimpse at devices still under development and device structures not yet found in the literature.

MOSFET Modeling & BSIM3 User's Guide-Yuhua Cheng 2007-05-08

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Circuit simulation is essential in integrated circuit design, and the accuracy of circuit simulation depends on the accuracy of the transistor model. BSIM3v3 (BSIM for Berkeley Short-channel IGFET Model) has been selected as the first MOSFET model for standardization by the Compact Model Council, a consortium of leading companies in semiconductor and design tools. In the next few years, many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3. This will require many device engineers and most circuit designers to learn the basics of BSIM3. MOSFET Modeling & BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs, and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters. It is the first book devoted to BSIM3. It treats the BSIM3 model in detail as used in digital, analog and RF circuit design. It covers the complete set of models, i.e., I-V model, capacitance model, noise model, parasitics model, substrate current model, temperature effect model and non quasi-static model. MOSFET Modeling & BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital/analog circuit design, RF modeling, statistical modeling, and technology prediction. This book is written for circuit designers and device engineers, as well as device scientists worldwide. It is also suitable as a reference for graduate courses and courses in circuit design or device modelling. Furthermore, it can be used as a textbook for industry courses devoted to BSIM3. MOSFET Modeling & BSIM3 User's Guide is comprehensive and practical. It is balanced between the background information and advanced discussion of BSIM3. It is helpful to experts and students alike.

Silicon Analog Components-Badih El-Kareh 2019-08-07 This book covers modern analog components, their characteristics, and interactions with process parameters. It serves as a comprehensive guide, addressing both the theoretical and practical aspects of modern silicon devices and the relationship between their electrical properties and processing conditions. Based on the authors' extensive experience in the development of analog devices, this

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book is intended for engineers and scientists in semiconductor research, development and manufacturing. The problems at the end of each chapter and the numerous charts, figures and tables also make it appropriate for use as a text in graduate and advanced undergraduate courses in electrical engineering and materials science. Enables engineers to understand analog device physics, and discusses important relations between process integration, device design, component characteristics, and reliability; Describes in step-by-step fashion the components that are used in analog designs, the particular characteristics of analog components, while comparing them to digital applications; Explains the second-order effects in analog devices, and trade-offs between these effects when designing components and developing an integrated process for their manufacturing.

Introduction to Semiconductor Device Modelling-Christopher M. Snowden 1998 This book deals mainly with physical device models which are developed from the carrier transport physics and device geometry considerations. The text concentrates on silicon and gallium arsenide devices and includes models of silicon bipolar junction transistors, junction field effect transistors (JFETs), MESFETs, silicon and GaAs MESFETs, transferred electron devices, pn junction diodes and Schottky varactor diodes. The modelling techniques of more recent devices such as the heterojunction bipolar transistors (HBT) and the high electron mobility transistors are discussed. This book contains details of models for both equilibrium and non-equilibrium transport conditions. The modelling Technique of Small-scale devices is discussed and techniques applicable to submicron-dimensioned devices are included. A section on modern quantum transport analysis techniques is included. Details of essential numerical schemes are given and a variety of device models are used to illustrate the application of these techniques in various fields.

The Physics of Semiconductors-Kevin F. Brennan 1999-02-13 Graduate text with comprehensive treatment of semiconductor device physics and engineering, and descriptions of real optoelectronic devices.

Atomic Layer Deposition for Semiconductors-Cheol Seong Hwang 2013-10-18 Offering thorough coverage of atomic layer deposition

(ALD), this book moves from basic chemistry of ALD and modeling of processes to examine ALD in memory, logic devices and machines. Reviews history, operating principles and ALD processes for each device.

Lateral Power Transistors in Integrated Circuits-Tobias Erlbacher 2014-10-08 The book summarizes and compares recent advancements in the development of novel lateral power transistors (LDMOS devices) for integrated circuits in power electronic applications. In its first part, the book motivates the necessity for lateral power transistors by a top-down approach: First, it presents typical energy conversion applications in modern industrial, automotive and consumer electronics. Next, it introduces common circuit topologies suitable for these applications, and discusses the feasibility for monolithic integration. Finally, the combination of power and logic functionality on a single chip is motivated and the requirements and limitations for the power semiconductor devices are deduced. The second part describes the evolution of lateral power transistors over the past decades from the simple pin-type concept to double-acting RESURF topologies. It describes the principle of operation for these LDMOS devices and discusses limitations of lateral power devices. Moreover, figures-of-merit are presented which can be used to evaluate the performance of the novel lateral power transistors described in this book with respect to the LDMOS devices. In the last part, [...] the fundamental physical concepts including charge compensation and trench gate topologies are discussed. Also, the status of research in LDMOS devices on silicon carbide is presented. Advantages and drawbacks for each of these integration approaches are summarized, and the feasibility with respect to power electronic applications is evaluated.

Integrated Circuit Failure Analysis-Friedrich Beck 1998-02-04 Fault analysis of highly-integrated semiconductor circuits has become an indispensable discipline in the optimization of product quality. Integrated Circuit Failure Analysis describes state-of-the-art procedures for exposing suspected failure sites in semiconductor devices. The author adopts a hands-on problem-oriented approach, founded on many years of practical experience, complemented by the explanation of basic theoretical principles. Features include: Advanced methods in device preparation and technical procedures

for package inspection and semiconductor reliability. Illustration of chip isolation and step-by-step delayering of chips by wet chemical and modern plasma dry etching techniques. Particular analysis of bipolar and MOS circuits, although techniques are equally relevant to other semiconductors. Advice on the choice of suitable laboratory equipment. Numerous photographs and drawings providing guidance for checking results. Focusing on modern techniques, this practical text will enable both academic and industrial researchers and IC designers to expand the range of analytical and preparative methods at their disposal and to adapt to the needs of new technologies.

Advanced Theory of Semiconductor Devices-Karl Hess 2000

Electrical Engineering Advanced Theory of Semiconductor Devices  
Semiconductor devices are ubiquitous in today's world and are found increasingly in cars, kitchens and electronic door locks, attesting to their presence in our daily lives. This comprehensive book provides the fundamentals of semiconductor device theory from basic quantum physics to computer-aided design. Advanced Theory of Semiconductor Devices will improve your understanding of computer simulation of devices through a thorough discussion of basic equations, their validity, and numerical solutions as they are contained in current simulation tools. You will gain state-of-the-art knowledge of devices used in both III-V compounds and silicon technology. Specially featured are novel approaches and explanations of electronic transport, particularly in p-n junction diodes. Close attention is also given to innovative treatments of quantum-well laser diodes and hot electron effects in silicon technology. This in-depth book is written for engineers, graduate students, and research scientists in solid-state electronics who want to gain a better understanding of the principles underlying semiconductor devices.

Compound Semiconductor Device Modelling-Christopher M.

Snowden 2012-12-06 Compound semiconductor devices form the foundation of solid-state microwave and optoelectronic technologies used in many modern communication systems. In common with their low frequency counterparts, these devices are often represented using equivalent circuit models, but it is often necessary to resort to physical models in order to gain insight into

the detailed operation of compound semiconductor devices. Many of the earliest physical models were indeed developed to understand the 'unusual' phenomena which occur at high frequencies. Such was the case with the Gunn and IMPATI diodes, which led to an increased interest in using numerical simulation methods. Contemporary devices often have feature sizes so small that they no longer operate within the familiar traditional framework, and hot electron or even quantum mechanical models are required. The need for accurate and efficient models suitable for computer aided design has increased with the demand for a wider range of integrated devices for operation at microwave, millimetre and optical frequencies. The apparent complexity of equivalent circuit and physics-based models distinguishes high frequency devices from their low frequency counterparts . . Over the past twenty years a wide range of modelling techniques have emerged suitable for describing the operation of compound semiconductor devices. This book brings together for the first time the most popular techniques in everyday use by engineers and scientists. The book specifically addresses the requirements and techniques suitable for modelling GaAs, InP. ternary and quaternary semiconductor devices found in modern technology.

Compact Models for Integrated Circuit Design (Open Access)-Samar K. Saha 2018-09-03 Compact Models for Integrated Circuit Design: Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer-aided design (CAD). Written by an author with more than 25 years of industry experience in semiconductor processes, devices, and circuit CAD, and more than 10 years of academic experience in teaching compact modeling courses, this first-of-its-kind book on compact SPICE models for very-large-scale-integrated (VLSI) chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices. Starting from basic semiconductor physics and covering state-of-the-art device regimes from conventional micron to nanometer, this text: Presents industry standard models for bipolar-junction transistors (BJTs), metal-oxide-semiconductor (MOS) field-effect-transistors (FETs), FinFETs, and tunnel field-effect transistors (TFETs), along with statistical MOS models Discusses the major

issue of process variability, which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit (IC) design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design: Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices. However, even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book.

The Physics of Semiconductors-Marius Grundmann 2015-12-24 The 3rd edition of this successful textbook contains ample material for a comprehensive upper-level undergraduate or beginning graduate course, guiding readers to the point where they can choose a special topic and begin supervised research. The textbook provides a balance between essential aspects of solid-state and semiconductor physics, on the one hand, and the principles of various semiconductor devices and their applications in electronic and photonic devices, on the other. It highlights many practical aspects of semiconductors such as alloys, strain, heterostructures, nanostructures, that are necessary in modern semiconductor research but typically omitted in textbooks. Coverage also includes additional advanced topics, such as Bragg mirrors, resonators, polarized and magnetic semiconductors, nanowires, quantum dots, multi-junction solar cells, thin film transistors, carbon-based nanostructures and transparent conductive oxides. The text derives explicit formulas for many results to support better understanding of the topics. The Physics of Semiconductors requires little or no prior knowledge of solid-state physics and evolved from a highly regarded two-semester course. In the third edition several topics are extended and treated in more depth including surfaces, disordered materials, amorphous semiconductors, polarons, thermopower and noise. More than 1800 references guide the reader to historic and current literature including original and

review papers and books.

ULSI Devices-C. Y. Chang 2000-05-01 A complete guide to current knowledge and future trends in ULSI devices Ultra-Large-Scale Integration (ULSI), the next generation of semiconductor devices, has become a hot topic of investigation. ULSI Devices provides electrical and electronic engineers, applied physicists, and anyone involved in IC design and process development with a much-needed overview of key technology trends in this area. Edited by two of the foremost authorities on semiconductor device physics, with contributions by some of the best-known researchers in the field, this comprehensive reference examines such major ULSI devices as MOSFET, nonvolatile semiconductor memory (NVSM), and the bipolar transistor, and the improvements these devices offer in power consumption, low-voltage and high-speed operation, and system-on-chip for ULSI applications. Supplemented with introductory material and references for each chapter as well as more than 400 illustrations, coverage includes: \* The physics and operational characteristics of the different components \* The evolution of device structures the ultimate limitations on device and circuit performance \* Device miniaturization and simulation \* Issues of reliability and the hot carrier effect \* Digital and analog circuit building blocks \*An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department

An Introduction to Semiconductor Devices-Donald A. Neamen 2006 An Introduction to Semiconductor Devices by Donald Neamen provides an understanding of the characteristics, operations and limitations of semiconductor devices. In order to provide this understanding, the book brings together the fundamental physics of the semiconductor material and the semiconductor device physics. This new text provides an accessible and modern presentation of material. Quantum mechanic material is minimal, and the most advanced material is designated with an icon. This modern approach means that coverage of the MOS transistor precedes the material on the bipolar transistor, which reflects the dominance of MOS technology in today's world. Excellent pedagogy is present throughout the book in the form of interesting chapters openers, worked examples, a variety of exercises, key terms, and end of

chapter problems.

Hot Carrier Degradation in Semiconductor Devices-Tibor Grasser 2014-10-29 This book provides readers with a variety of tools to address the challenges posed by hot carrier degradation, one of today's most complicated reliability issues in semiconductor devices. Coverage includes an explanation of carrier transport within devices and book-keeping of how they acquire energy ("become hot"), interaction of an ensemble of colder and hotter carriers with defect precursors, which eventually leads to the creation of a defect, and a description of how these defects interact with the device, degrading its performance.

Nonvolatile Semiconductor Memories-Chenming Hu 1991

Modern Semiconductor Quantum Physics-Ming-Fu Li 1995-02-01

Modern Semiconductor Quantum Physics has the following constituents: (1) energy band theory: pseudopotential method (empirical and ab initio); density functional theory; quasi-particles; LCAO method; k.p method; spin-orbit splitting; effective mass and Luttinger parameters; strain effects and deformation potentials; temperature effects. (2) Optical properties: absorption and exciton effect; modulation spectroscopy; photo luminescence and photo luminescence excitation; Raman scattering and polaritons; photoionization. (3) Defects and Impurities: effective mass theory and shallow impurity states; deep state cluster method, super cell method, Green's function method; carrier recombination kinetics; trapping transient measurements; electron spin resonance; electron lattice interaction and lattice relaxation effects; multi-phonon nonradiative recombination; negative U center, DX center and EL2 Defects. (4) Semiconductor surfaces: two dimensional periodicity and surface reconstruction; surface electronic states; photo-electron spectroscopy; LEED, STM and other experimental methods. (5) Low-dimensional structures: Heterojunctions, quantum wells; superlattices, quantum-confined Stark effect and Wannier-Stark ladder effects; resonant tunneling, quantum Hall effect, quantum wires and quantum dots. This book can be used as an advanced textbook on semiconductor physics for graduate students in physics and electrical engineering departments. It is also useful as a research reference for solid state scientists and semiconductor device engineers.

FinFET Modeling for IC Simulation and Design-Yogesh Singh Chauhan 2015-03-17 This book is the first to explain FinFET modeling for IC simulation and the industry standard - BSIM-CMG - describing the rush in demand for advancing the technology from planar to 3D architecture, as now enabled by the approved industry standard. The book gives a strong foundation on the physics and operation of FinFET, details aspects of the BSIM-CMG model such as surface potential, charge and current calculations, and includes a dedicated chapter on parameter extraction procedures, providing a step-by-step approach for the efficient extraction of model parameters. With this book you will learn: Why you should use FinFET The physics and operation of FinFET Details of the FinFET standard model (BSIM-CMG) Parameter extraction in BSIM-CMG FinFET circuit design and simulation Authored by the lead inventor and developer of FinFET, and developers of the BSIM-CM standard model, providing an experts' insight into the specifications of the standard The first book on the industry-standard FinFET model - BSIM-CMG

Semiconductor Device Modeling with Spice-Giuseppe Massabrio 1998-12-22 Publisher's Note: Products purchased from Third Party sellers are not guaranteed by the publisher for quality, authenticity, or access to any online entitlements included with the product.

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